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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1756	703/13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:36
L2	10	I1 and macromodel	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:38
L3	30	(703/13 703/2 703/15 716/7 716/18 716/4 702/5 702/8 702/109 702/118 702/2) and macromodel	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:39
L4	10	I3 and transmission	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:41
L5	0	macromodel and (transmission same line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:41
L6	24	macromodel and (transmission same line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:41
L7	8	macromodel and lossy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:41

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L8	26	macromodel and distortion	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:41
S1	10	(transmission near2 line) same macromodel	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/31 16:36
S2	0	("2005/0222831").URPN.	USPAT	OR	OFF	2007/07/21 18:56

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V Raghavan, JE Bracken, RA Rohrer - 1992 - IEEE Computer Society Press Los Alamitos, CA, USA

... **model** stencils. (Q +all SPICE3C1 **simulation** took 418.2 CPU-see. The time required

to create the AWE **macromodel** for the interconnect circuit was 0,7 CPU-see. ...

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A Odabasioglu, M Celik, LT Pileggi - Proceedings of the 1997 IEEE/ACM international conference on ..., 1997 - portal.acm.org

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